

FIG. 1

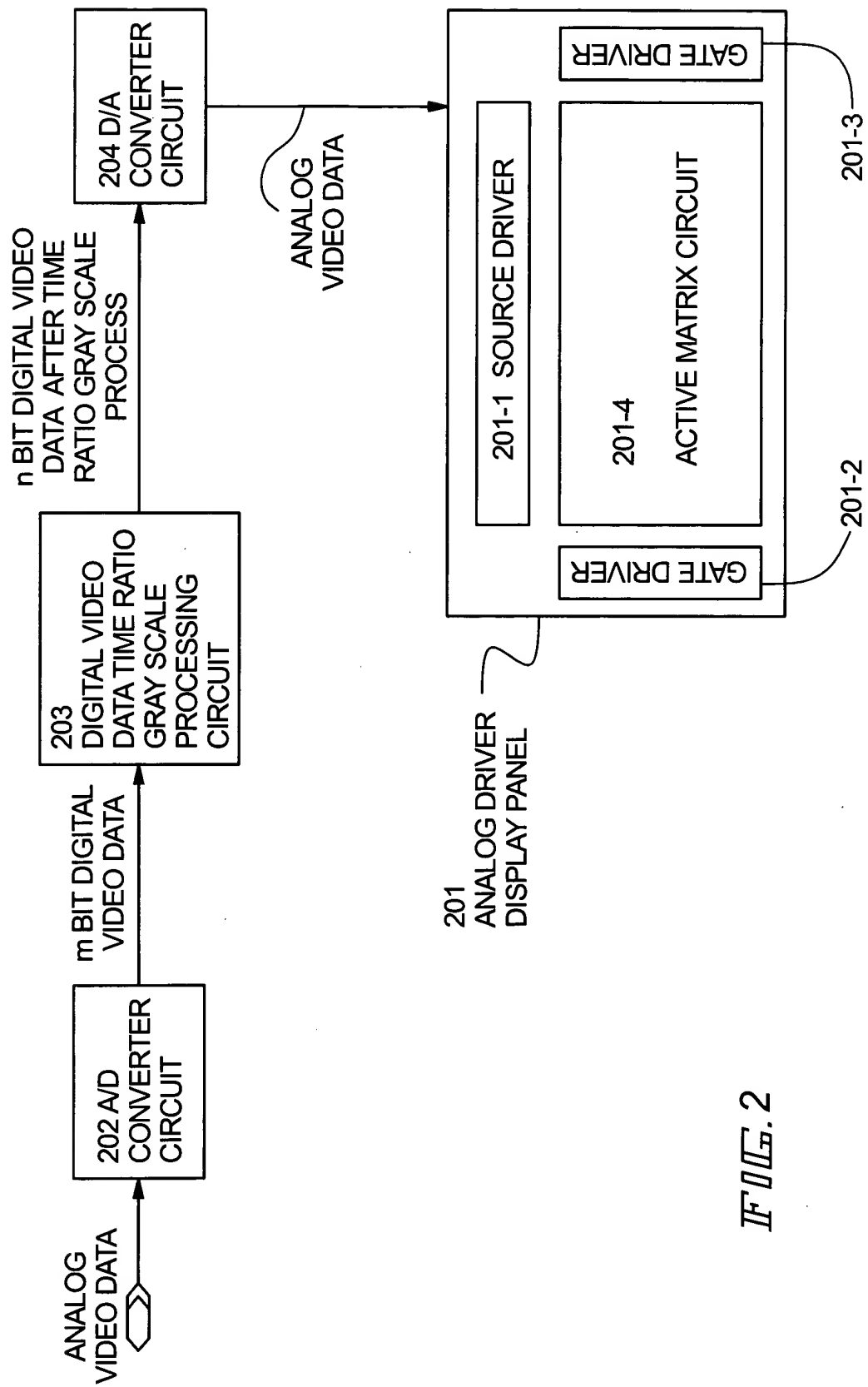


FIG. 2

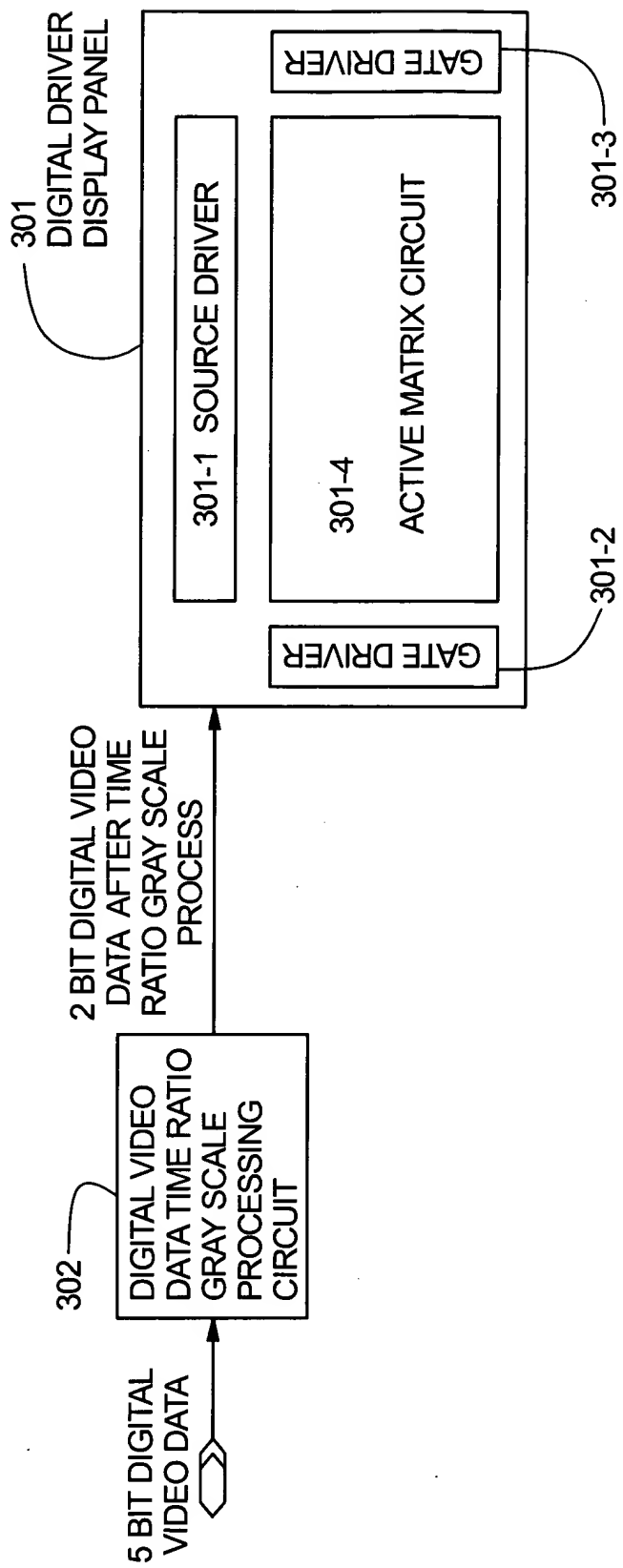


FIG. 3

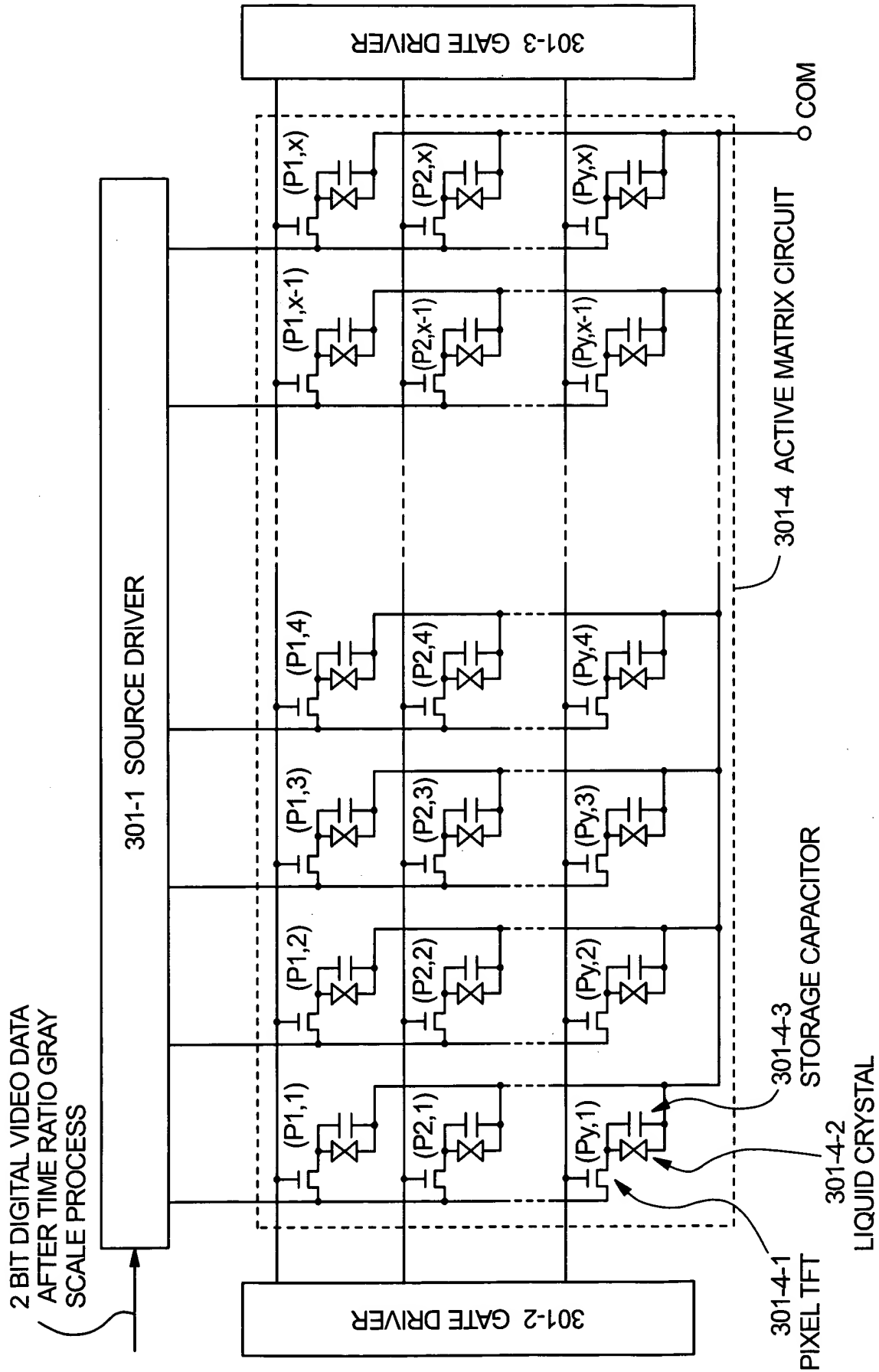


FIG. 4

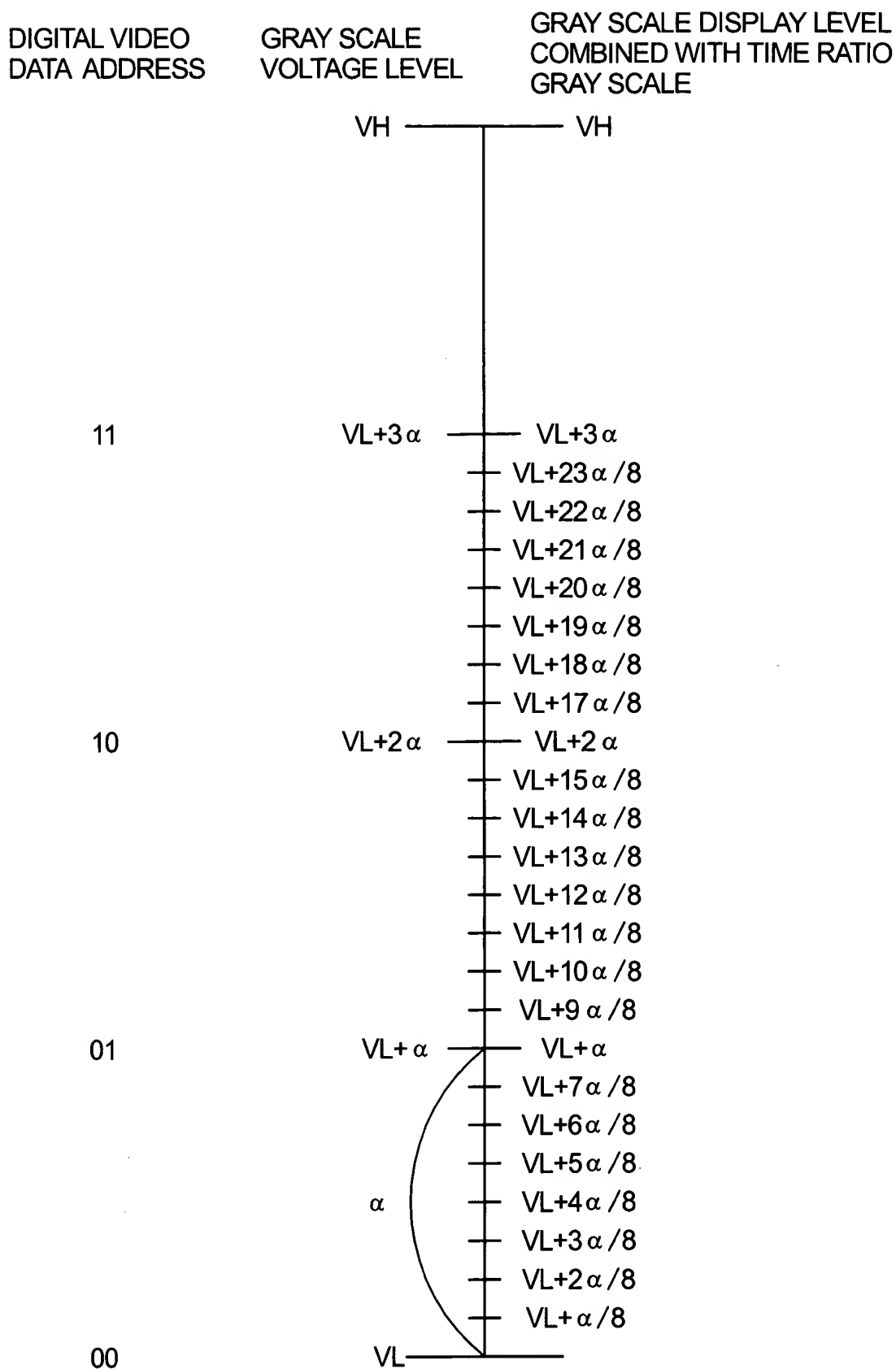


FIG. 5

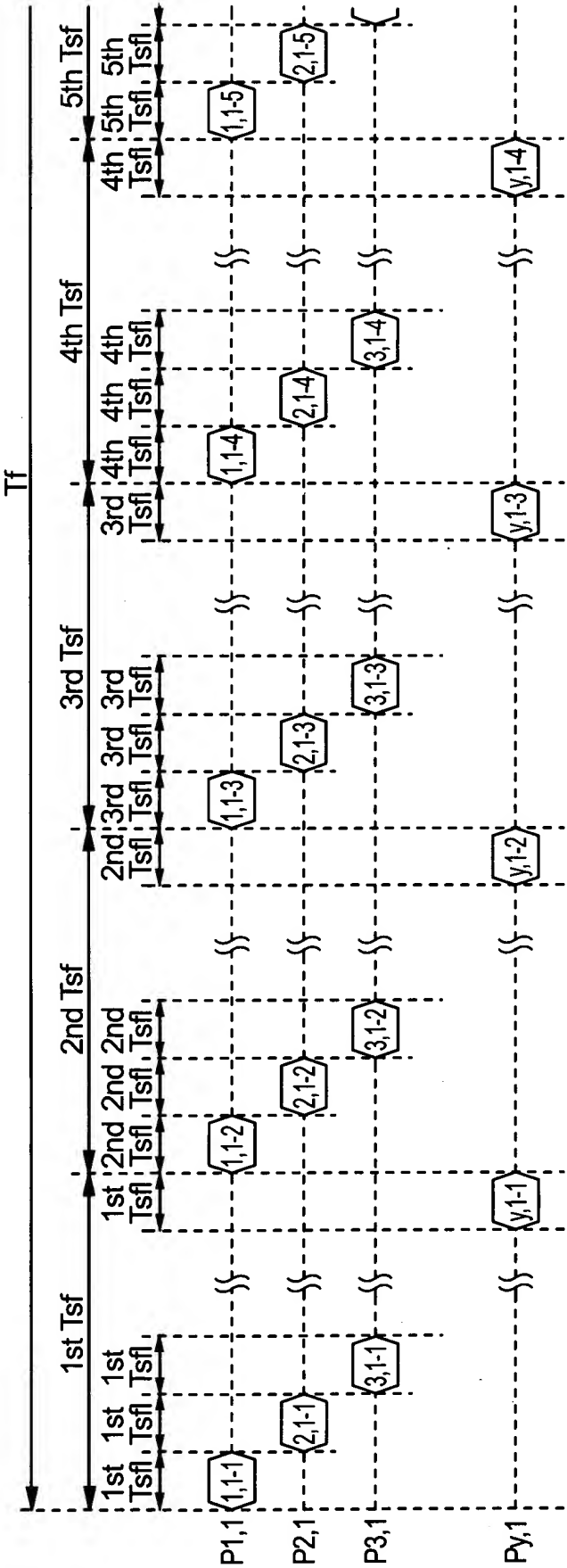


FIG. 6

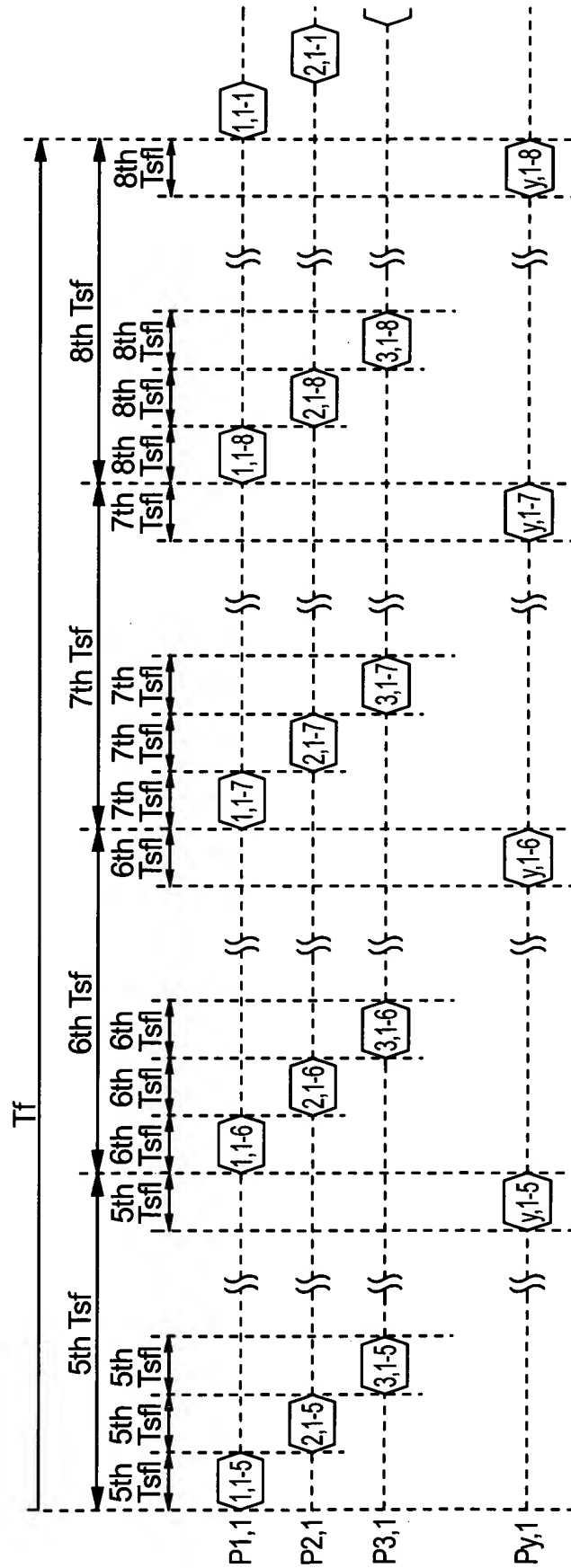


Fig. 7



FIG. 8

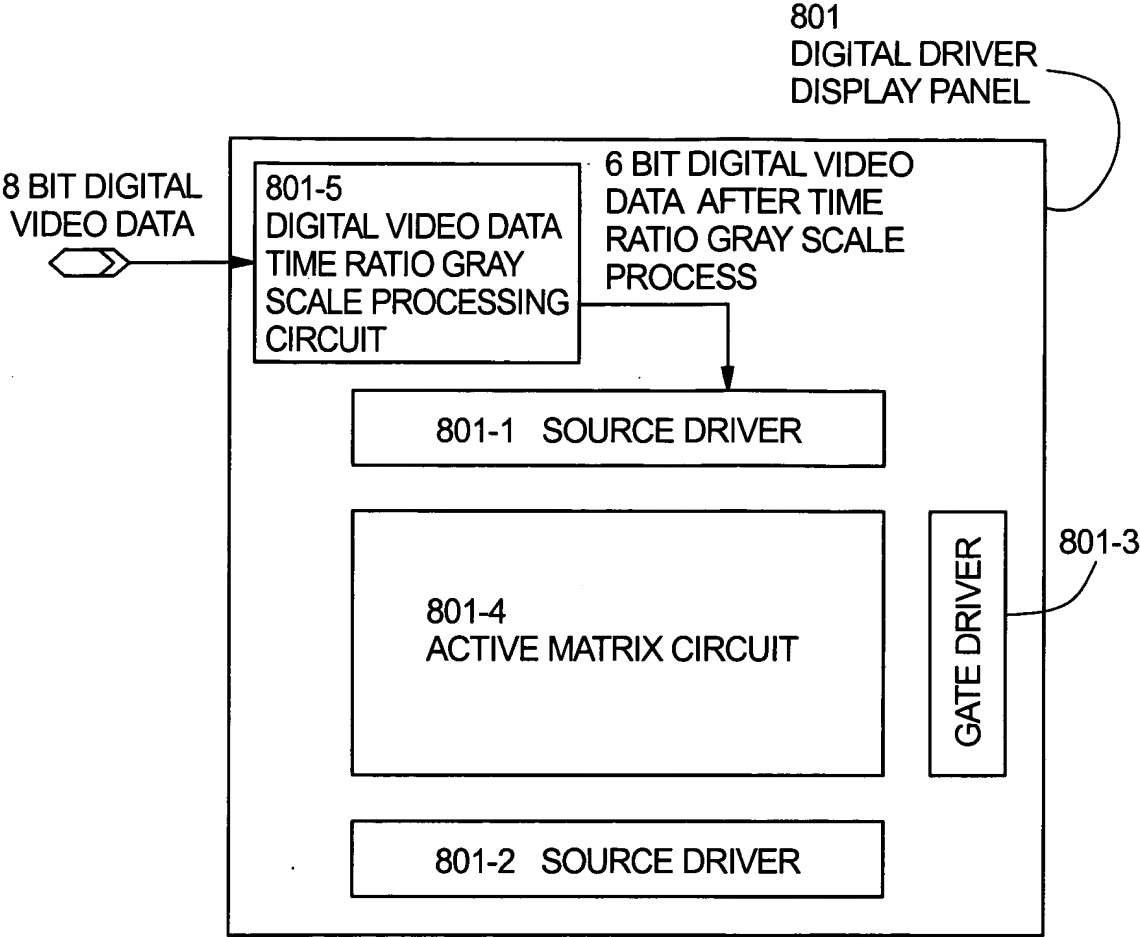


FIG. 9

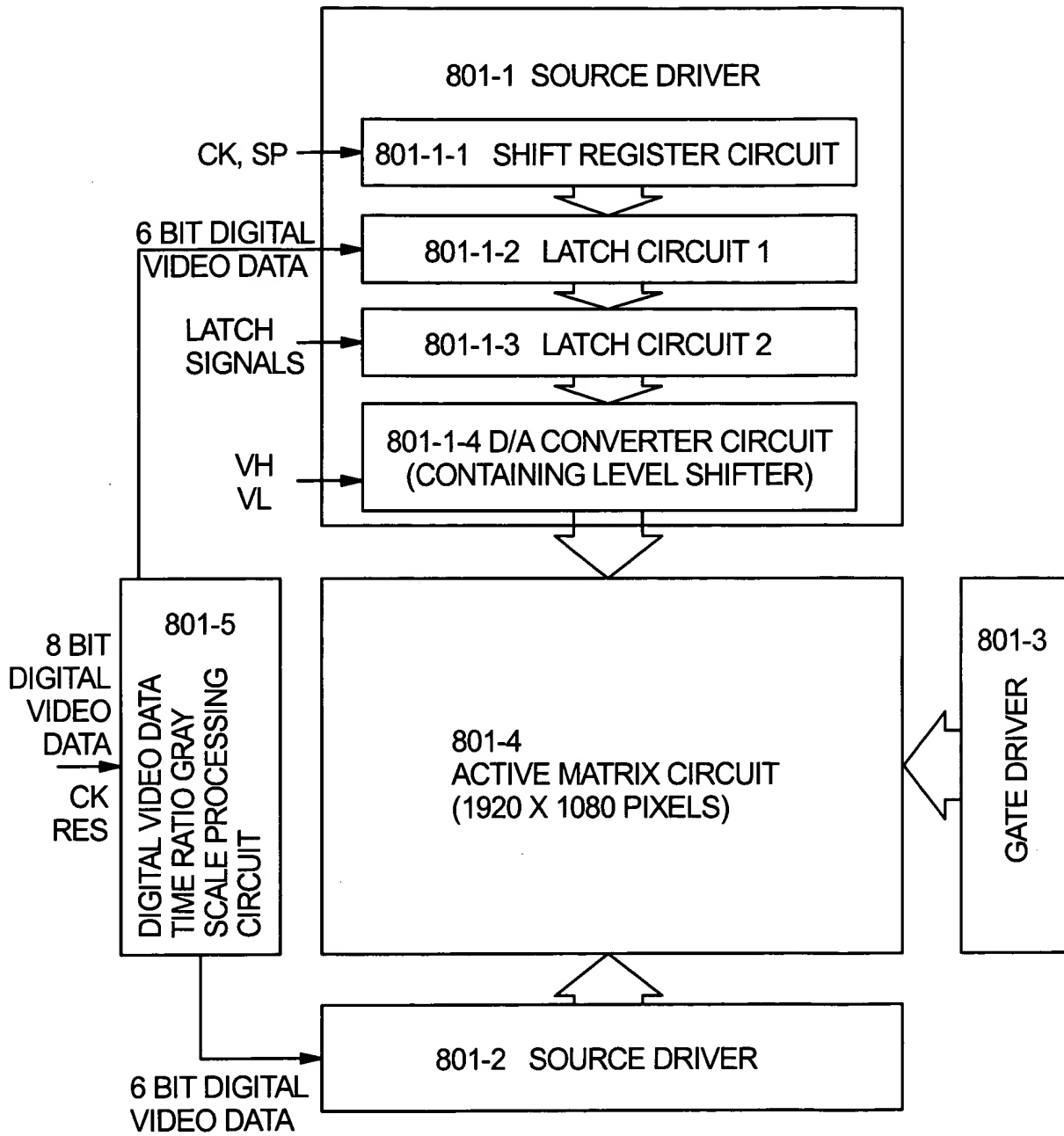


FIG. 10

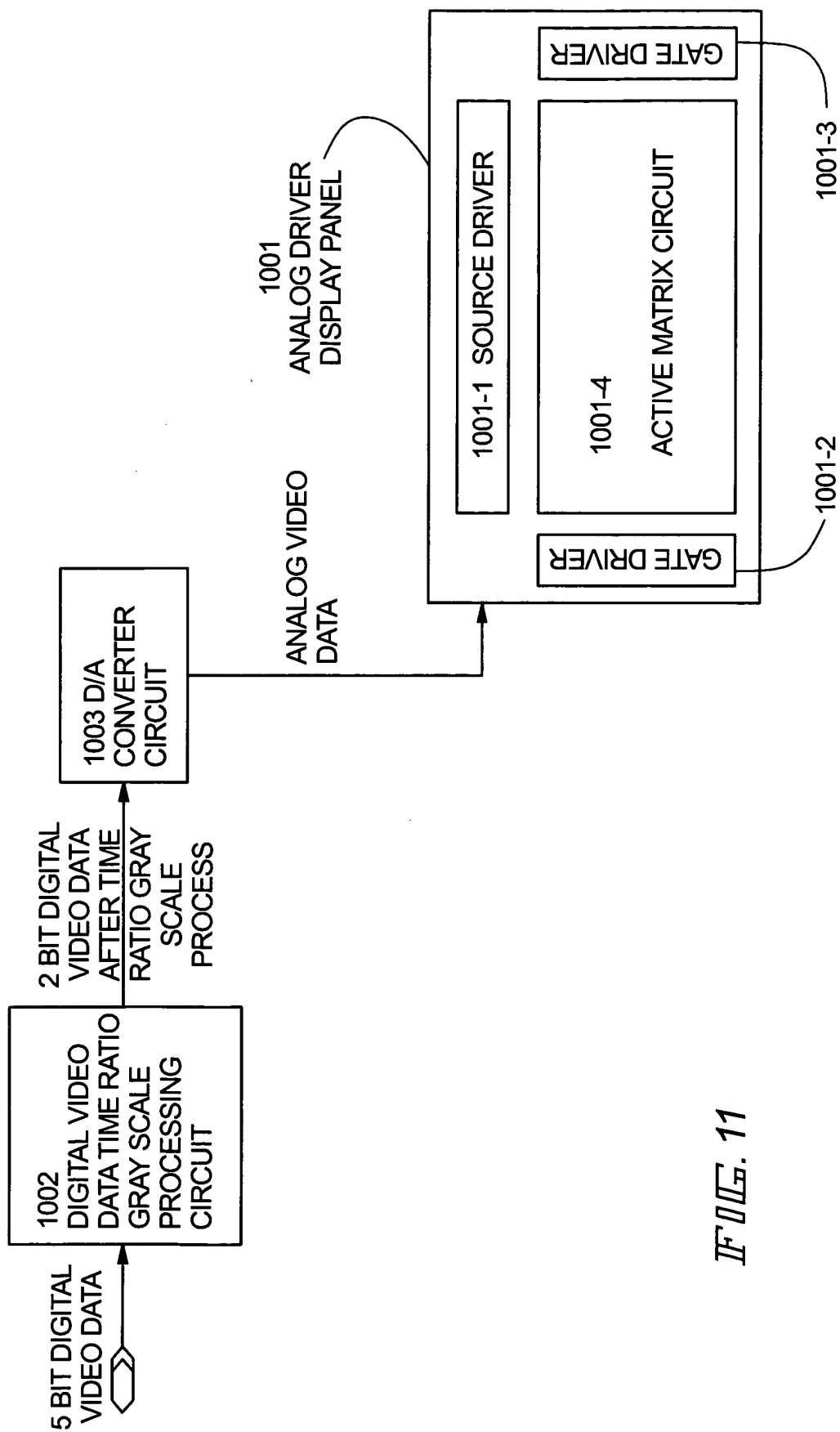


FIG. 11

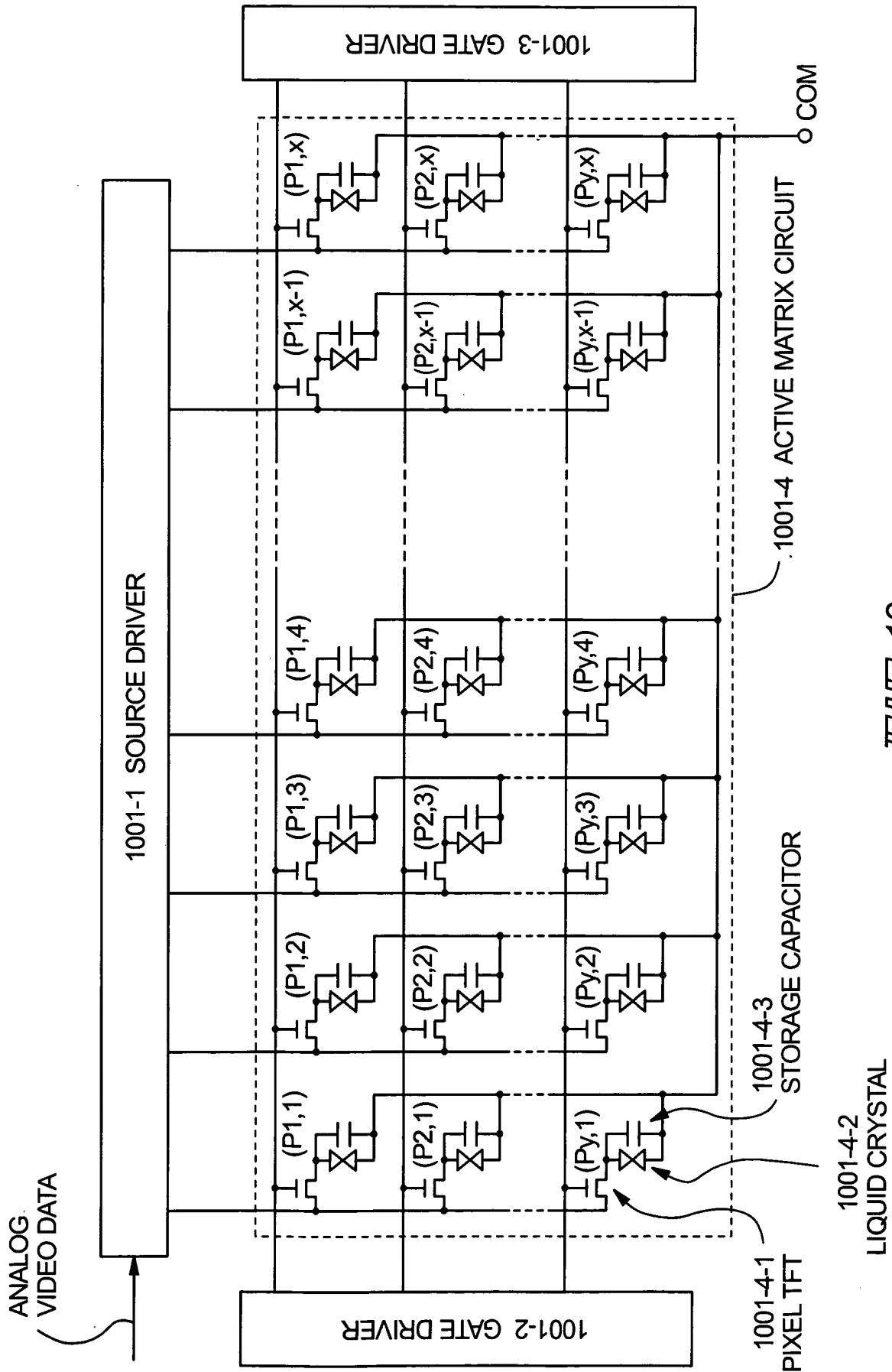
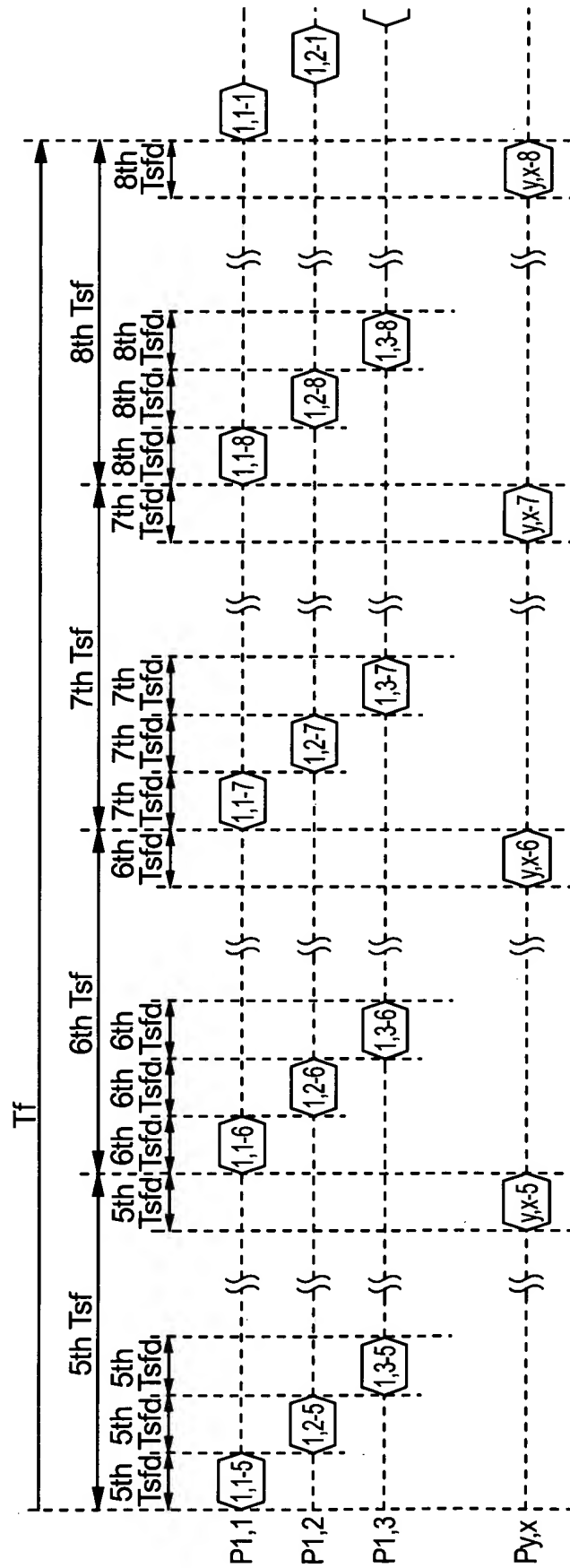


FIG. 12



FOLIO. 13



IF 006.14

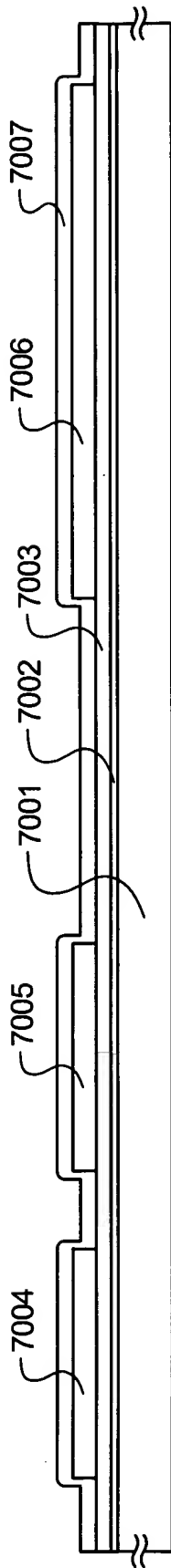


FIG. 15A

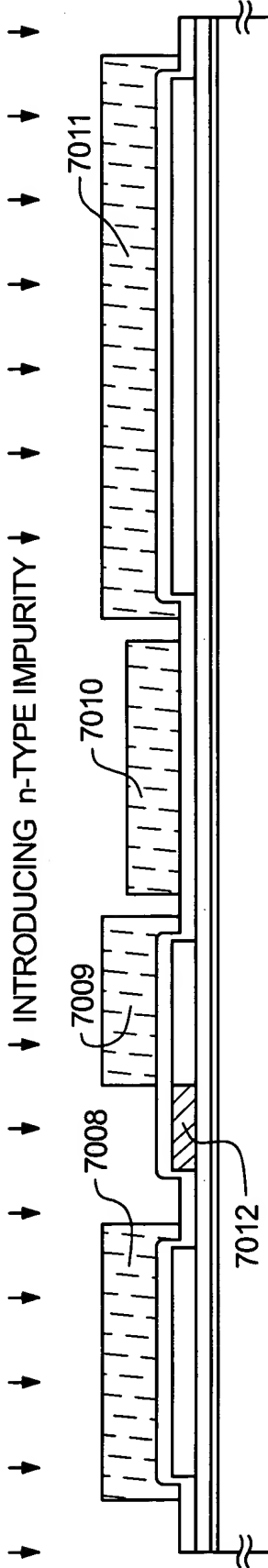


FIG. 15B

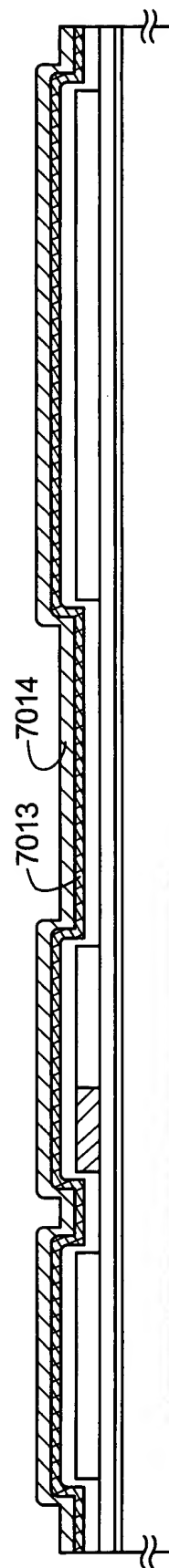


FIG. 15C

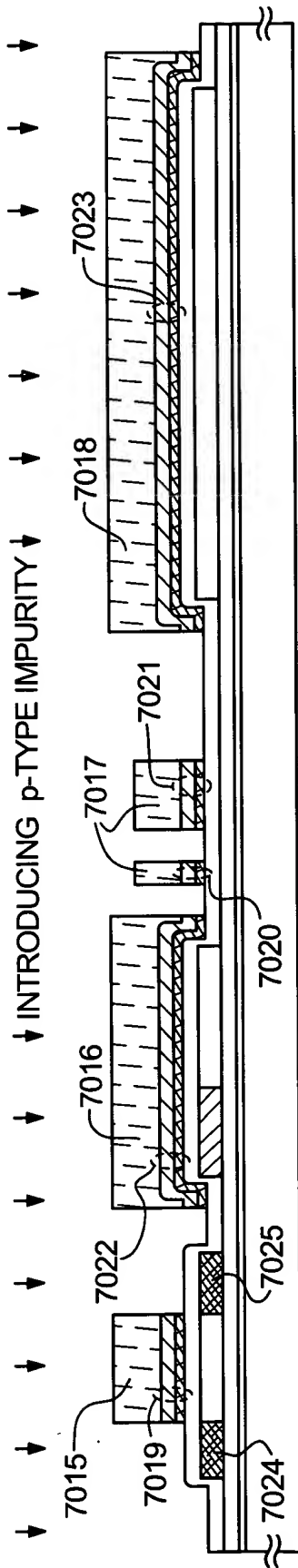


FIG. 16A

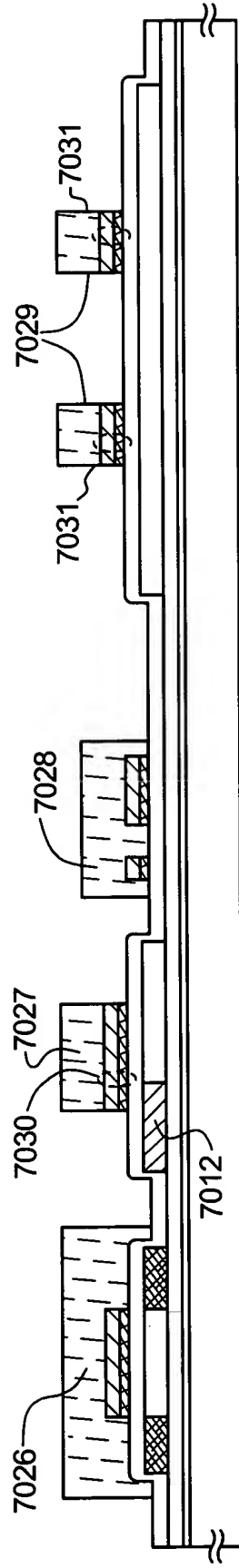


FIG. 16B

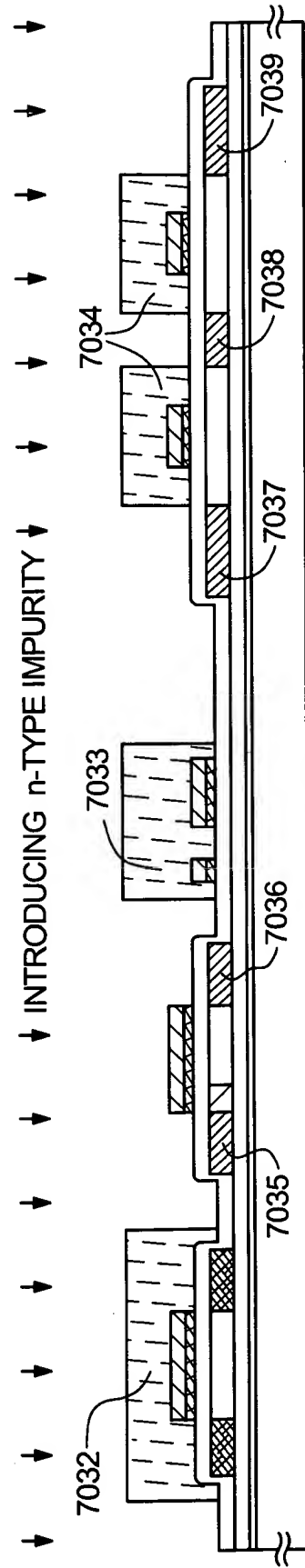


FIG. 16C

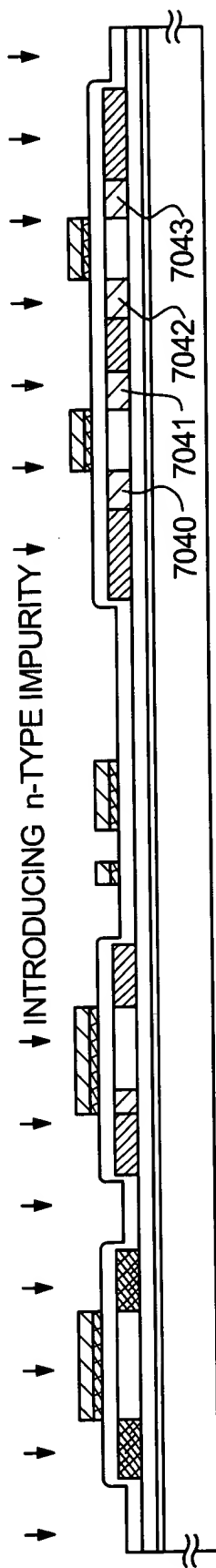


FIG. 17A

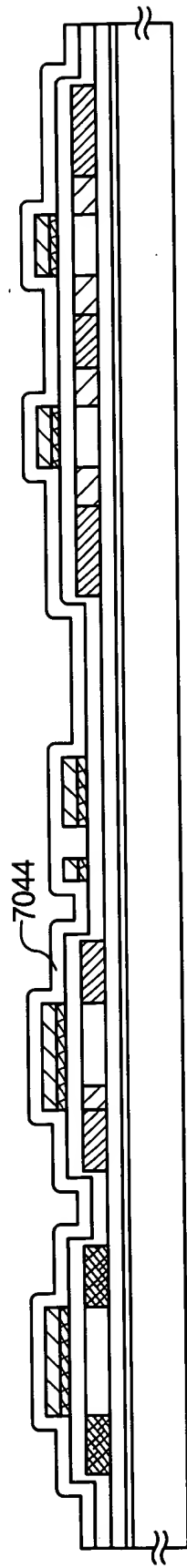
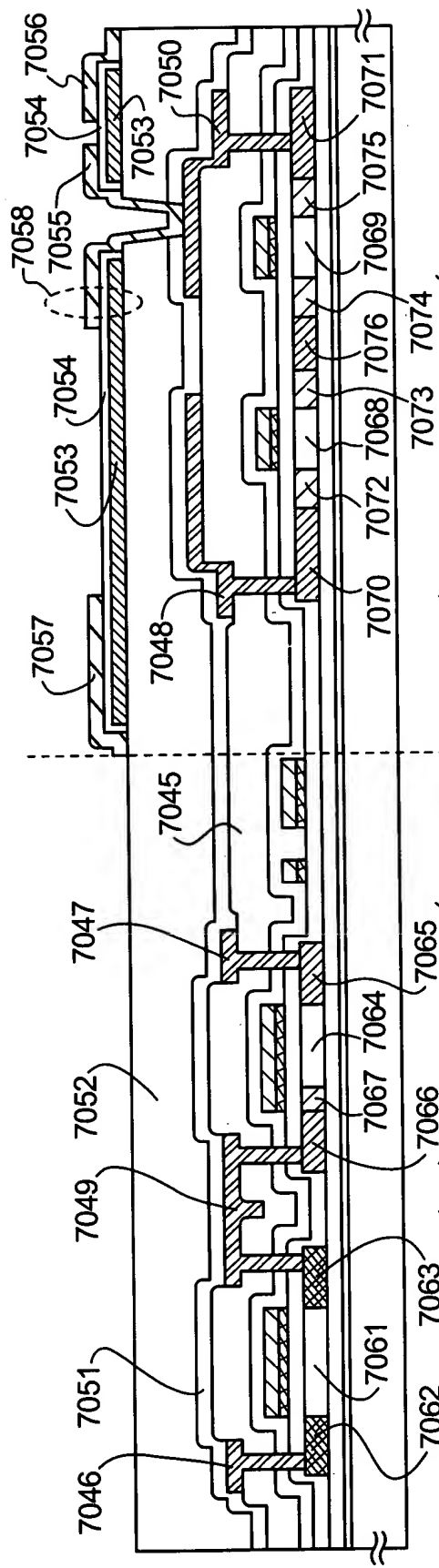


FIG. 17B



7081: p-CHANNEL TFT 7082: n-CHANNEL TFT
CMOS CIRCUIT

7083: n-CHANNEL TFT
PIXEL MATRIX CIRCUIT

FIG. 17C

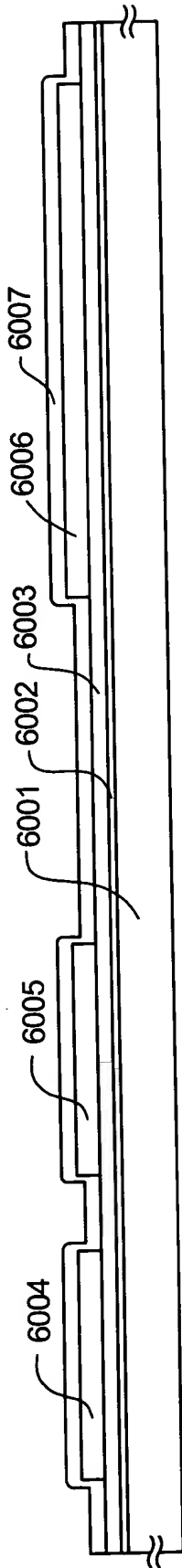


FIG. 18A

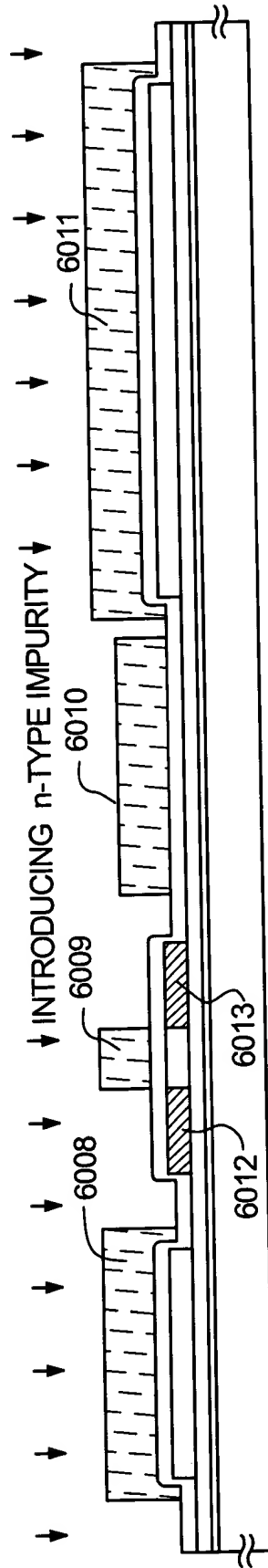


FIG. 18B

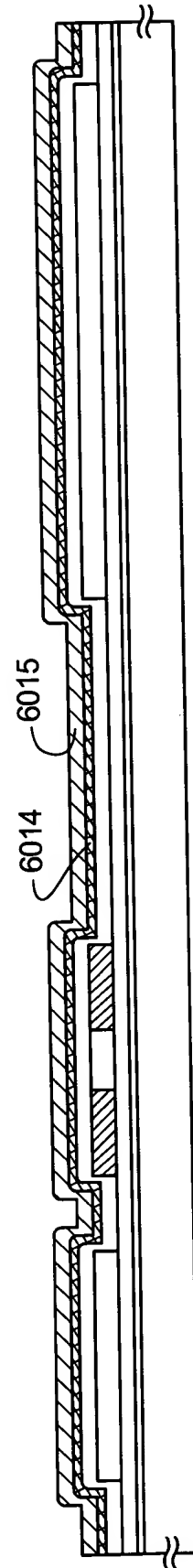


FIG. 18C

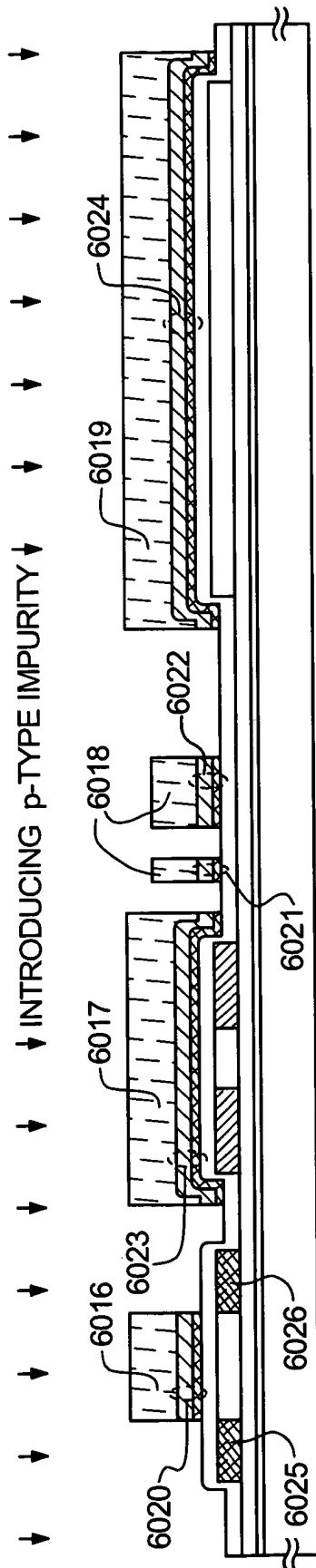


FIG. 19A

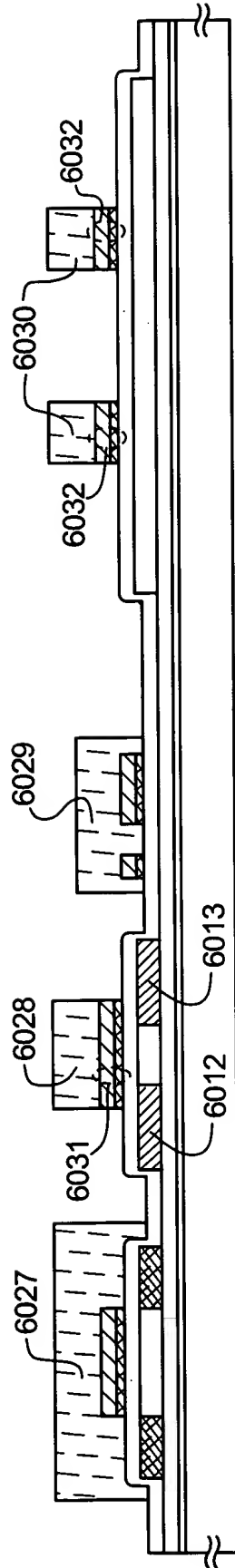


FIG. 19B

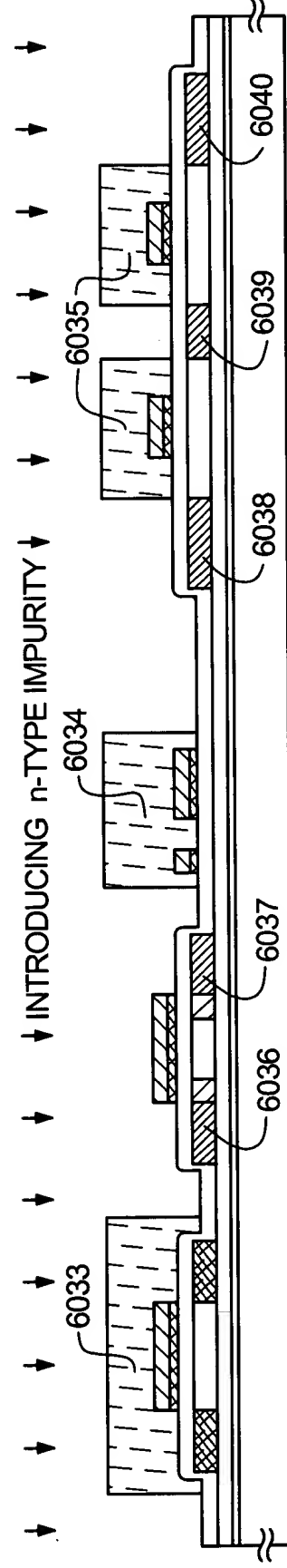


FIG. 19C

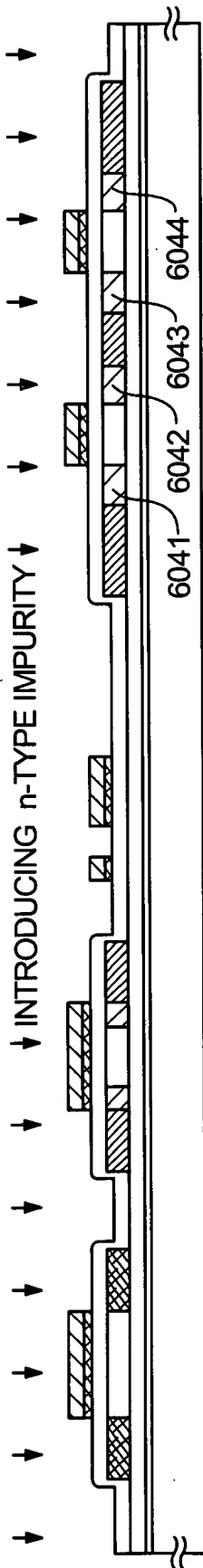


FIG. 20A

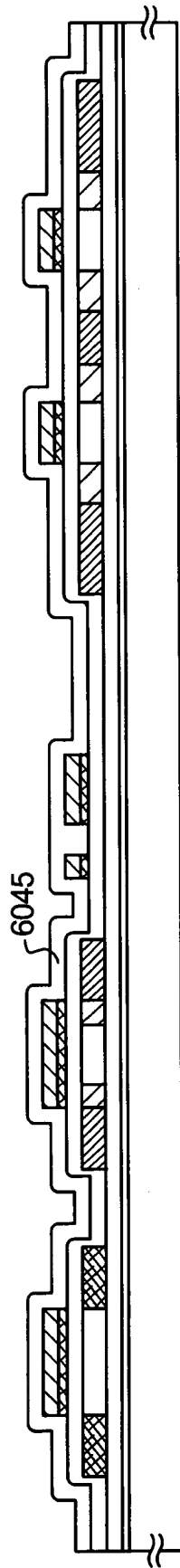
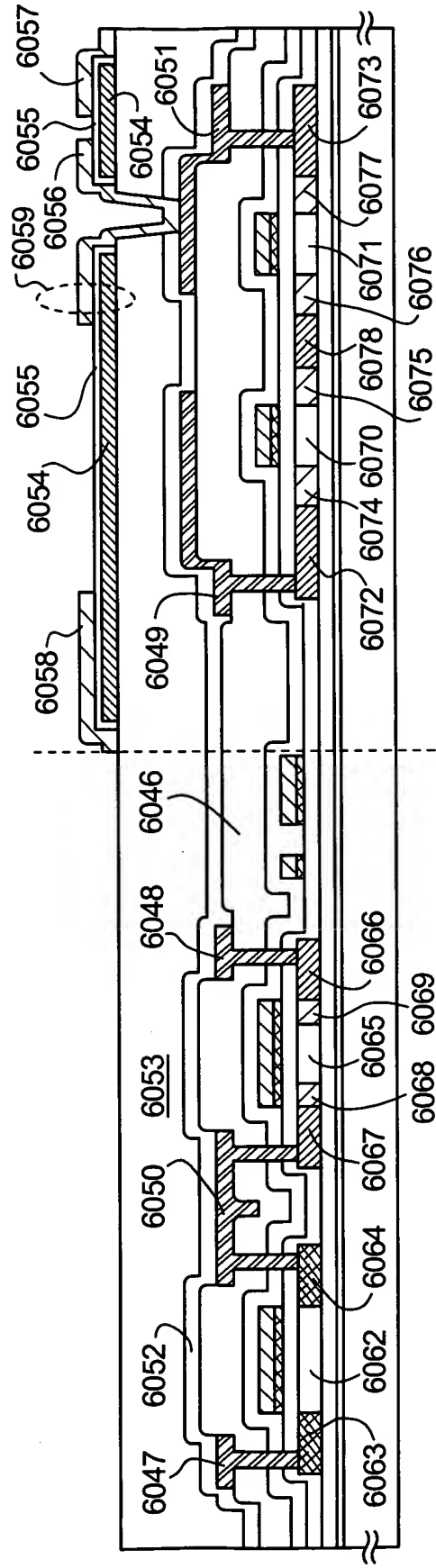


FIG. 20B



6081: p-CHANNEL TFT CMOS CIRCUIT
6083: n-CHANNEL TFT PIXEL MATRIX CIRCUIT
FIG. 20C

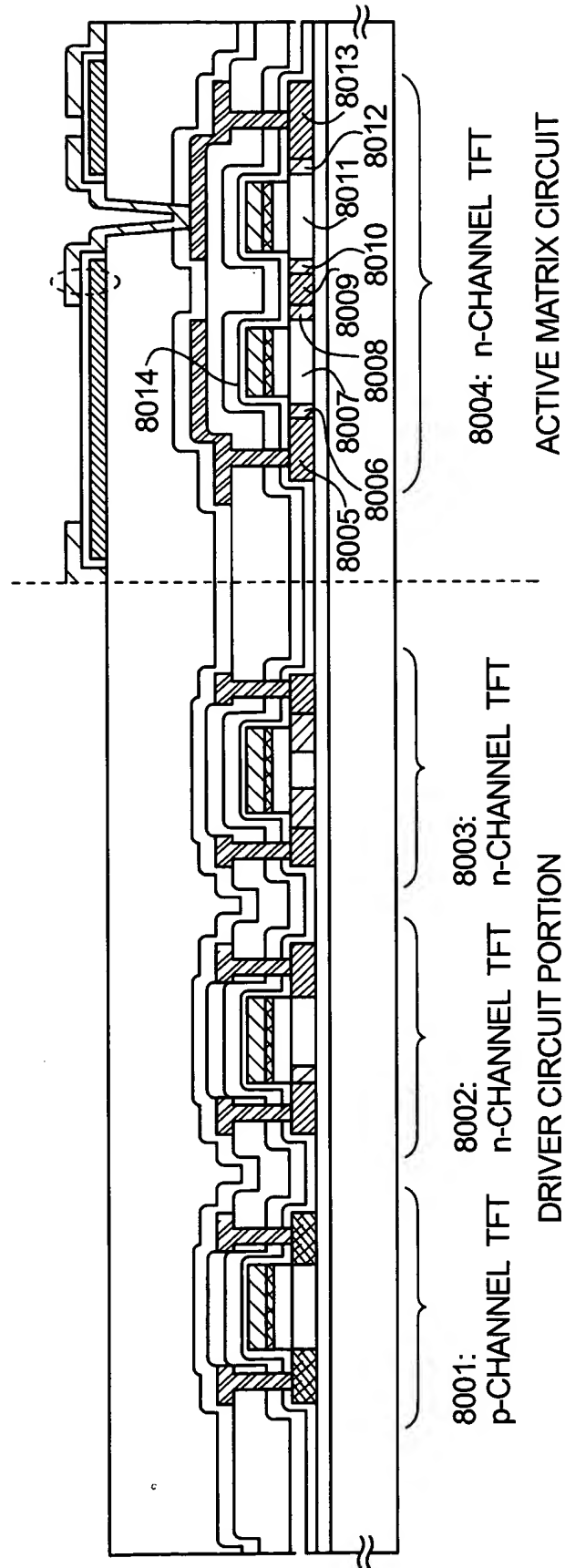


FIG. 21

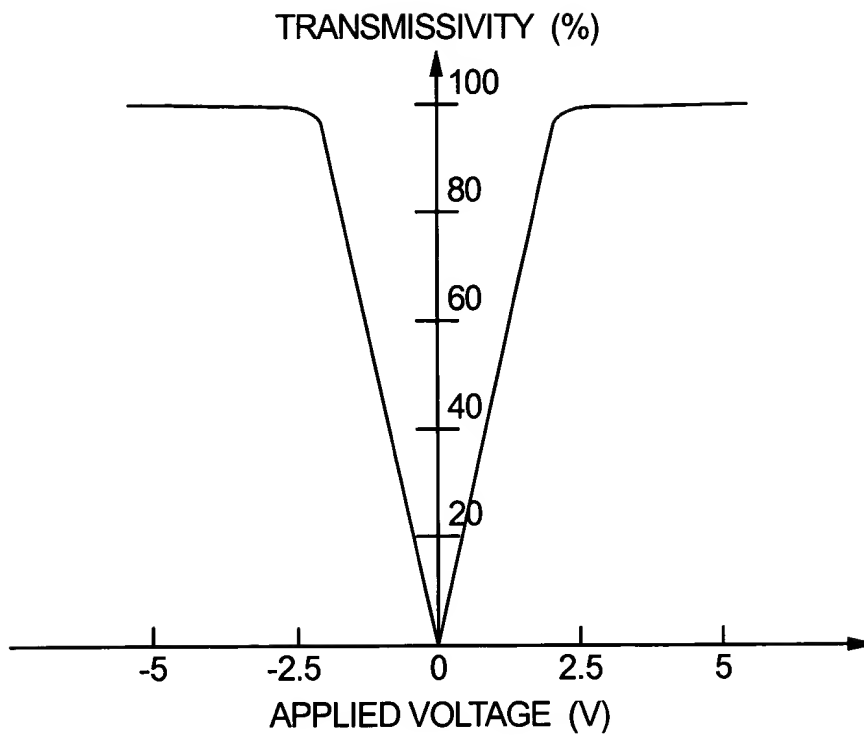


FIG. 22

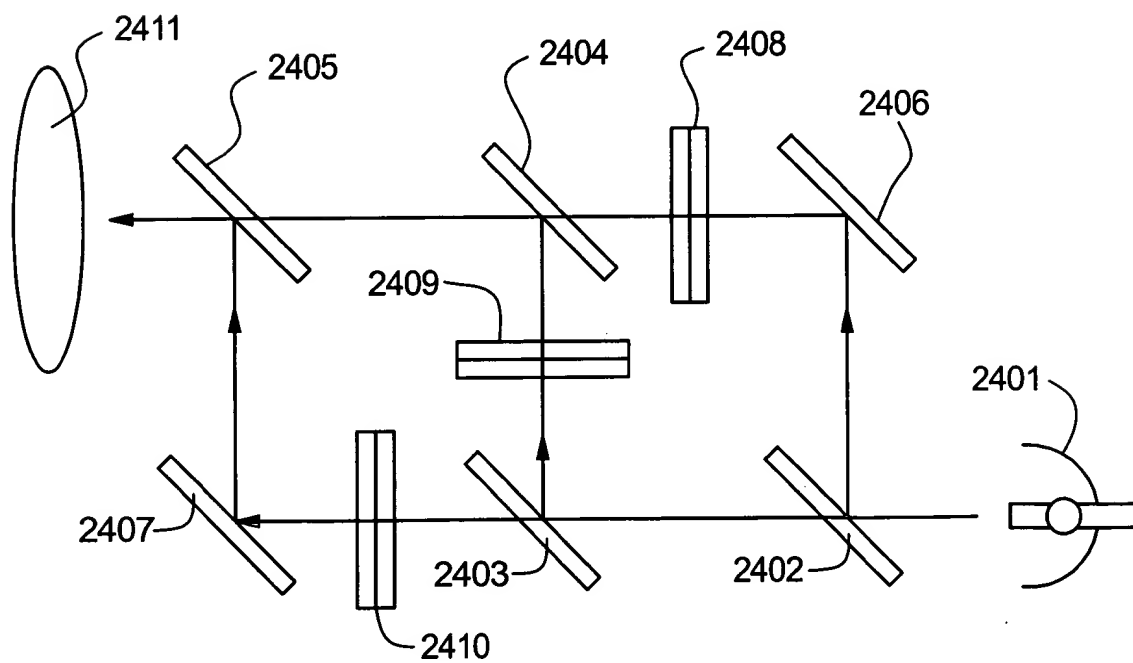


FIG. 23

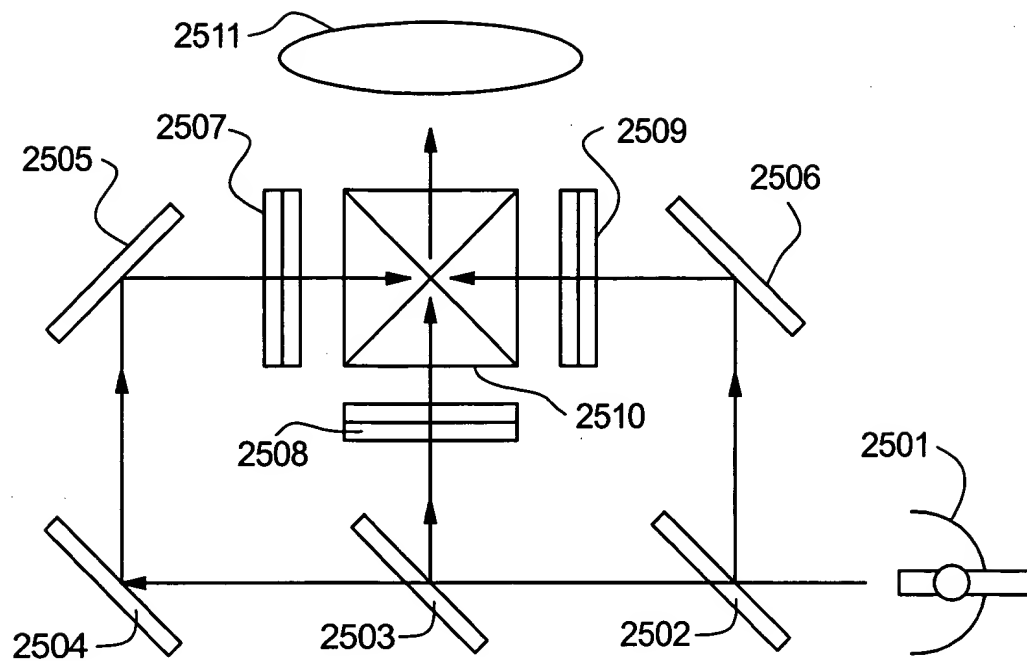


FIG. 24

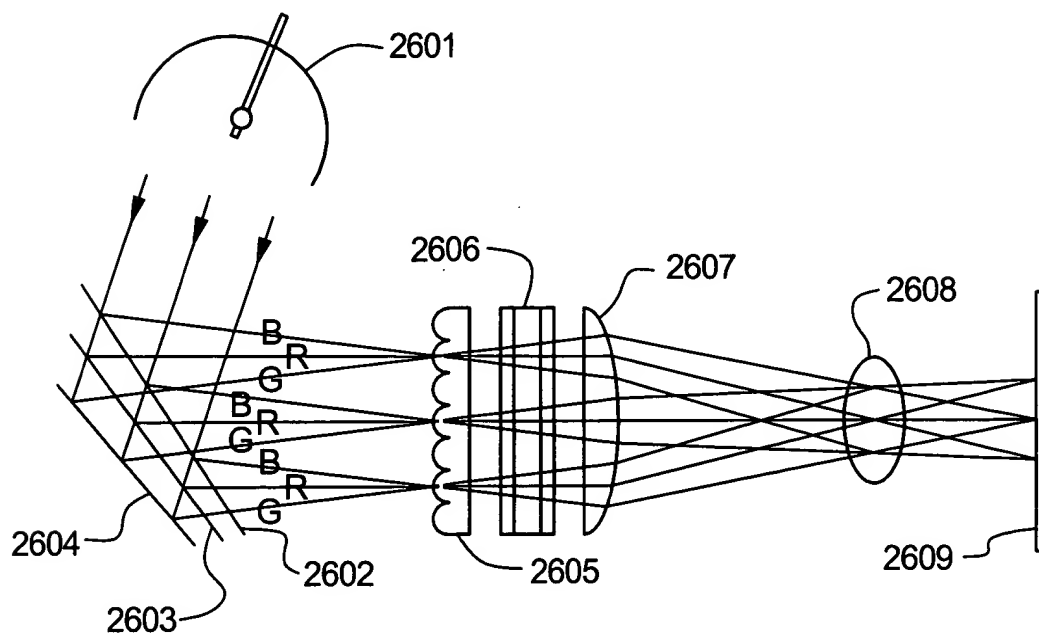


FIG. 25

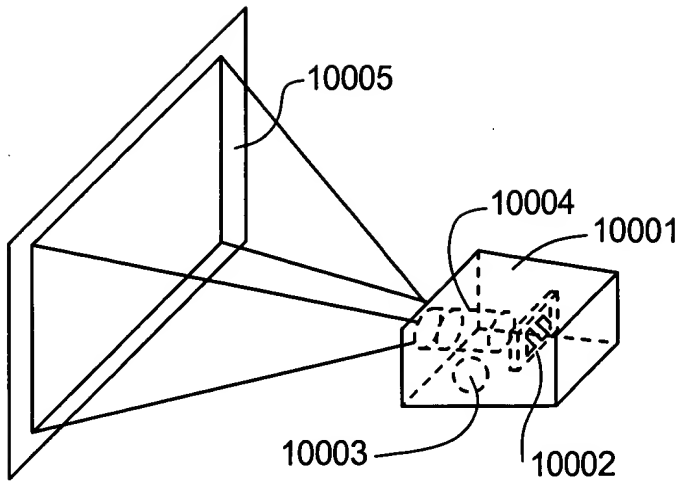


FIG. 26A

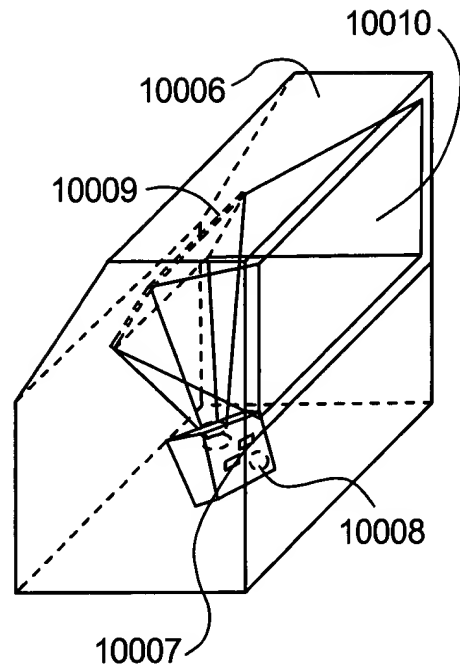


FIG. 26B

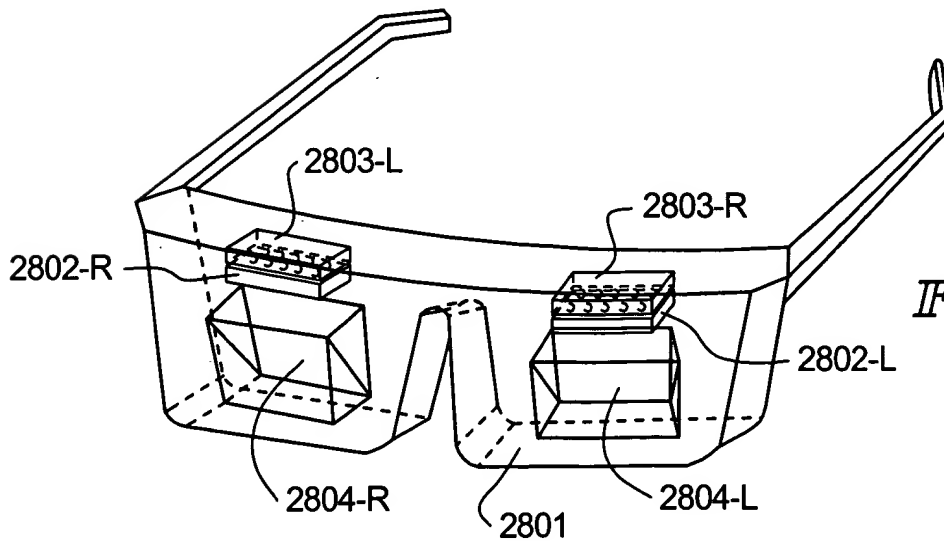


FIG. 27

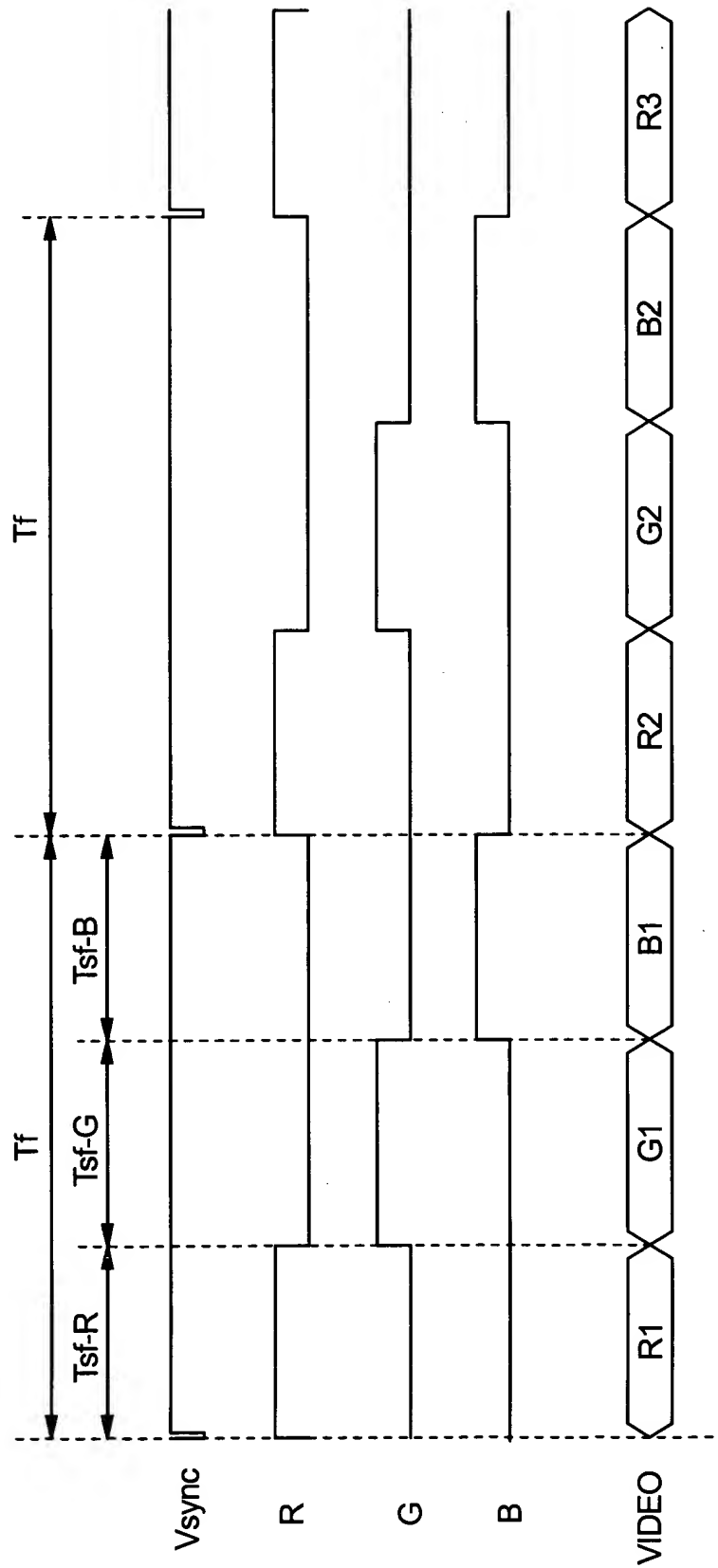


FIG. 28

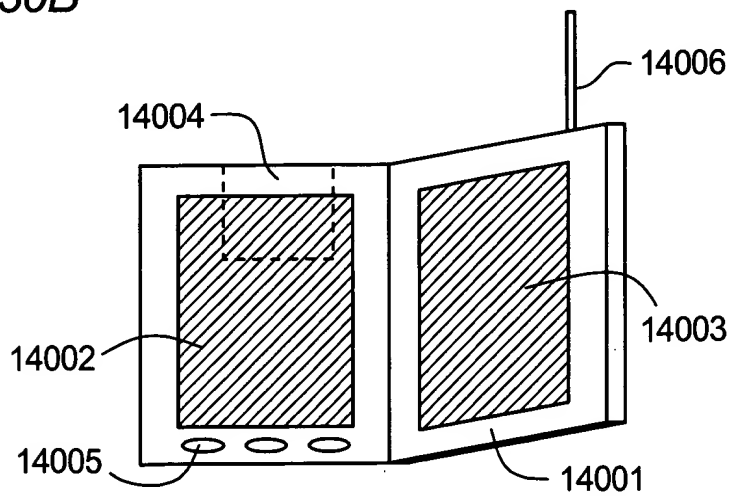
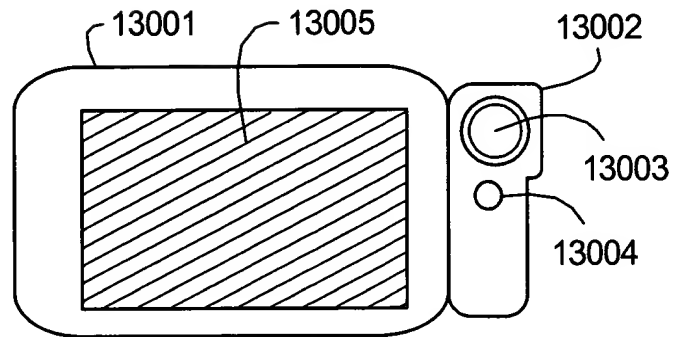
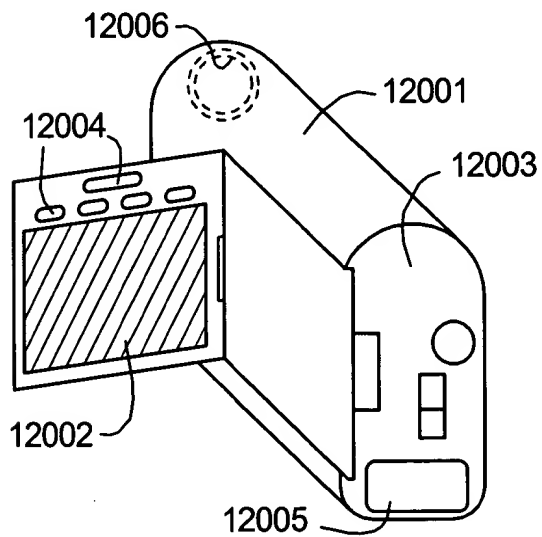
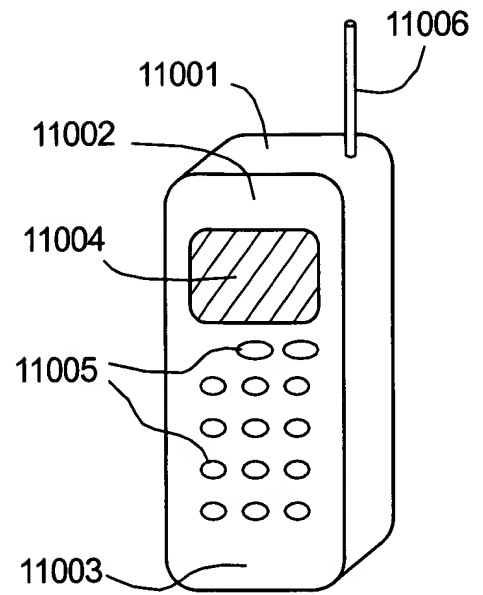
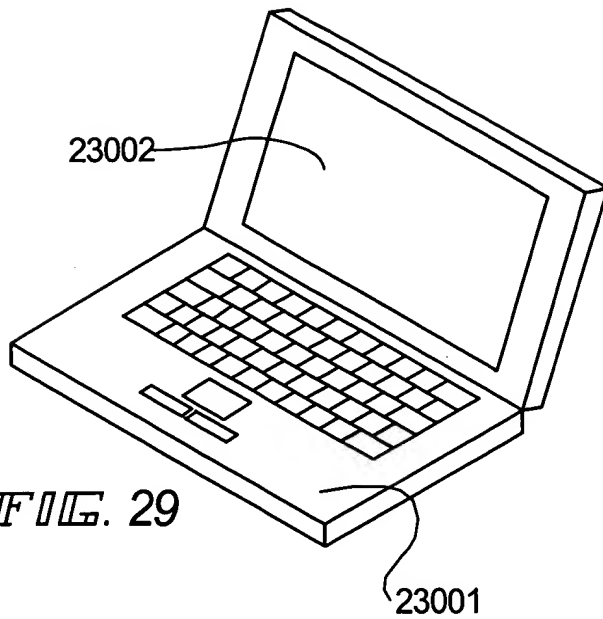


FIG. 30D

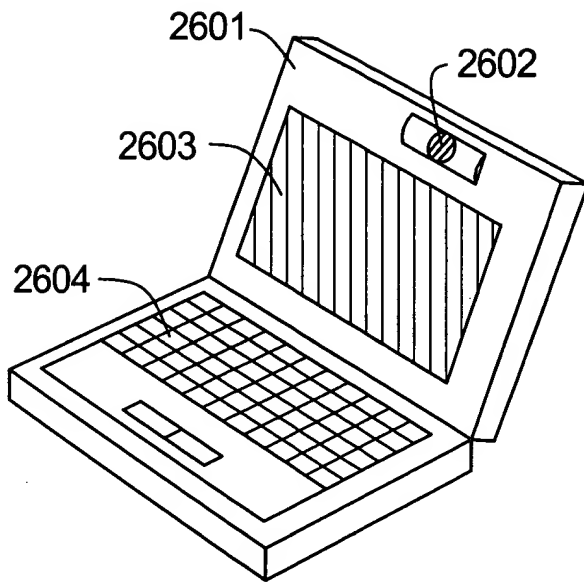


FIG. 31A

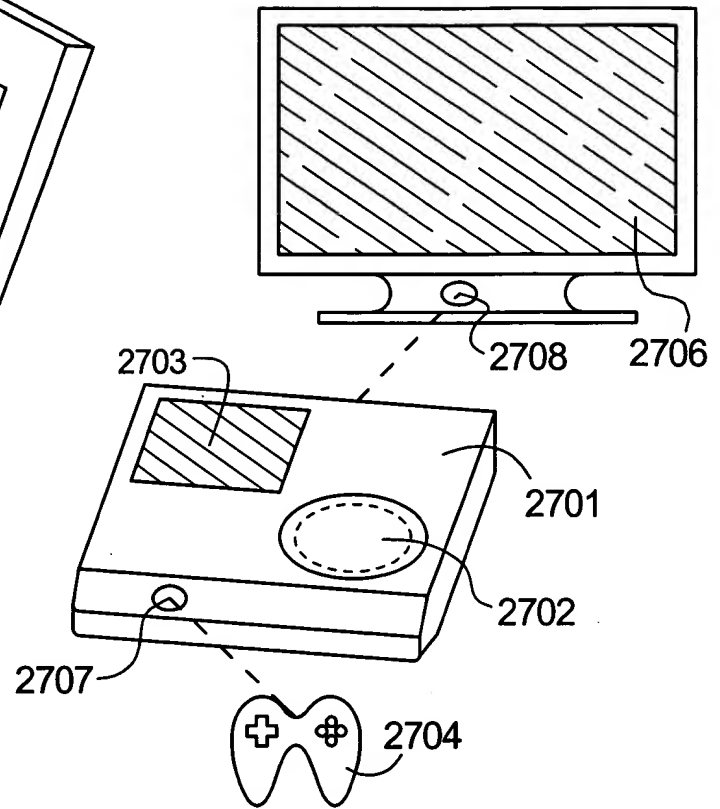


FIG. 31B

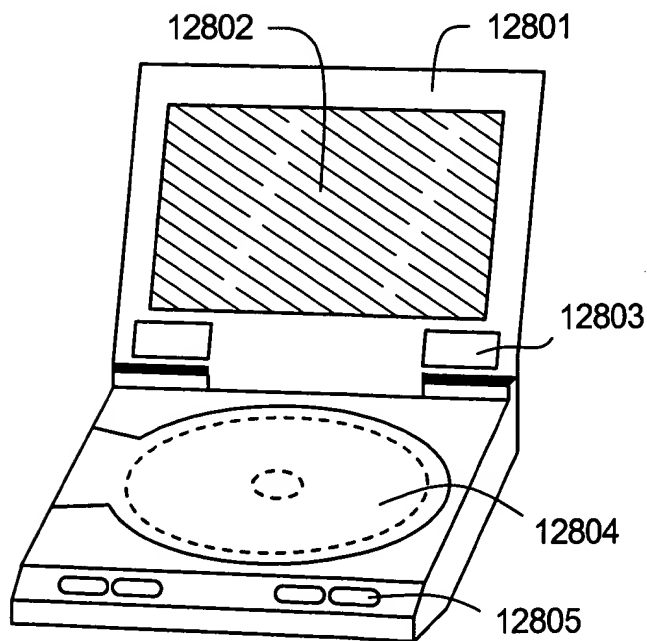


FIG. 31C

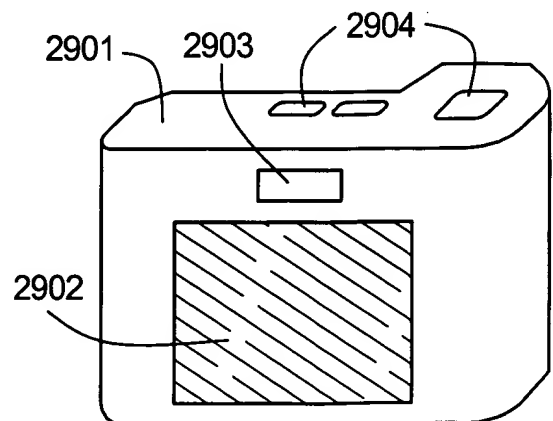


FIG. 31D

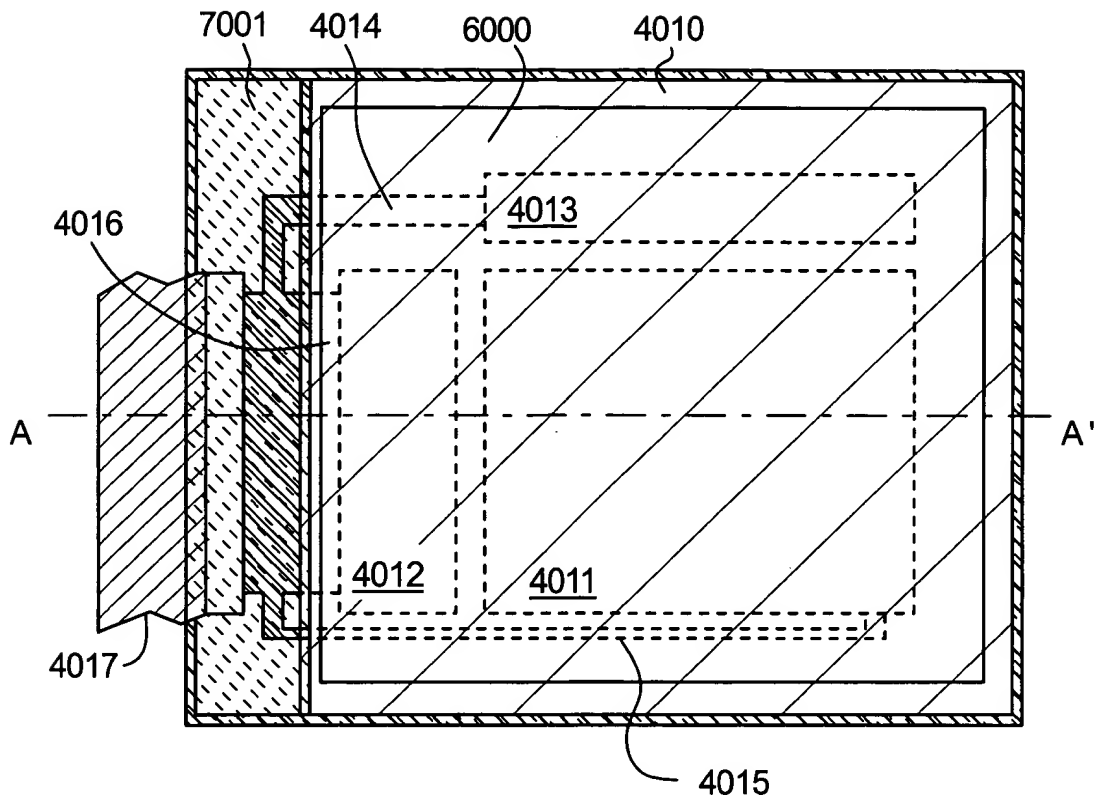


FIG. 32A

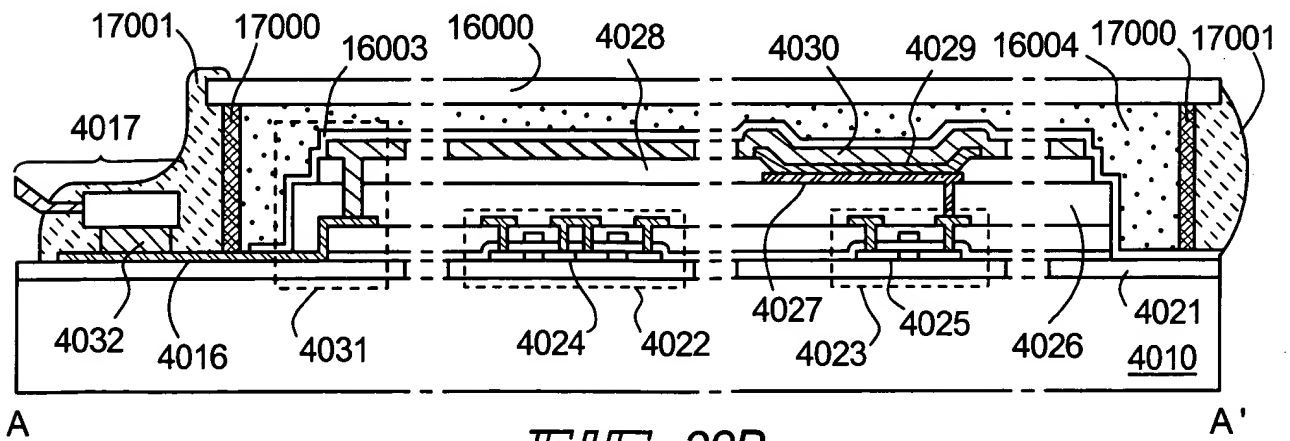


FIG. 32B

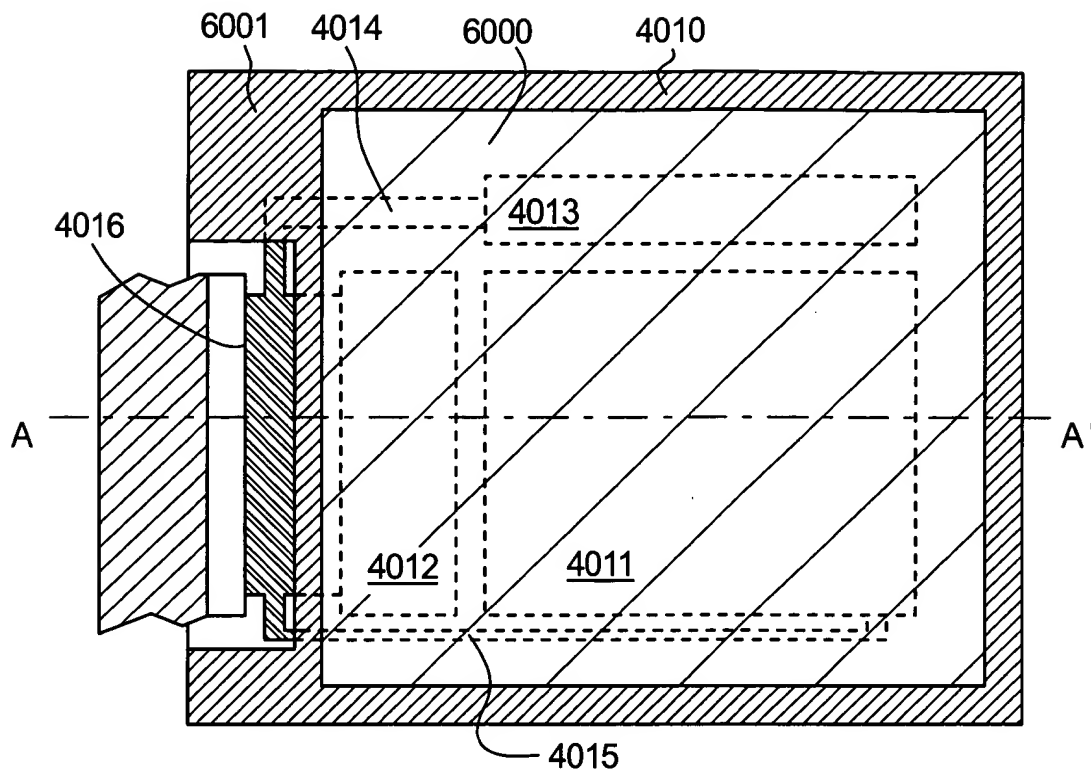


FIG. 33A

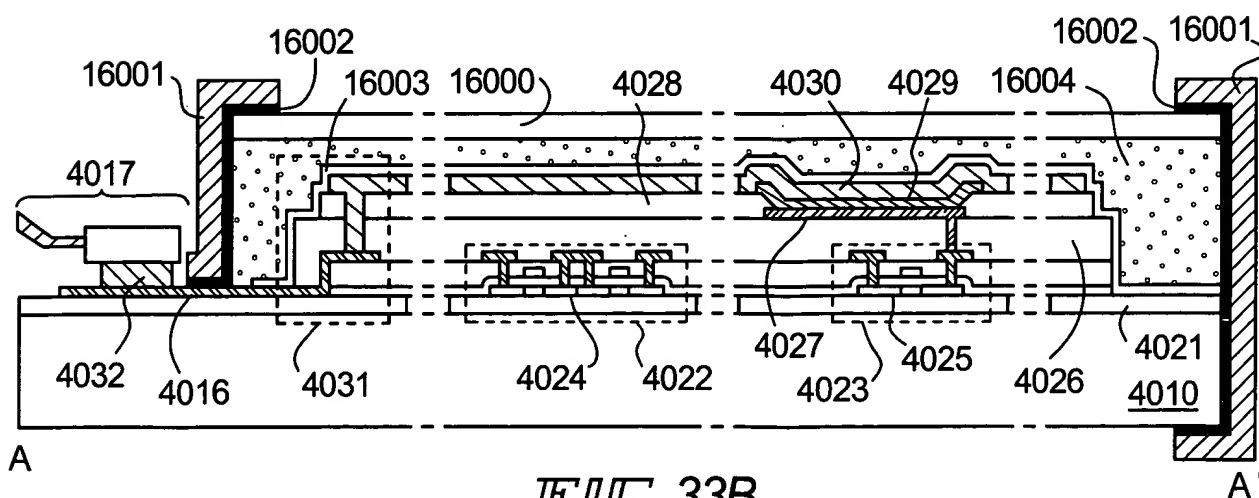


FIG. 33B

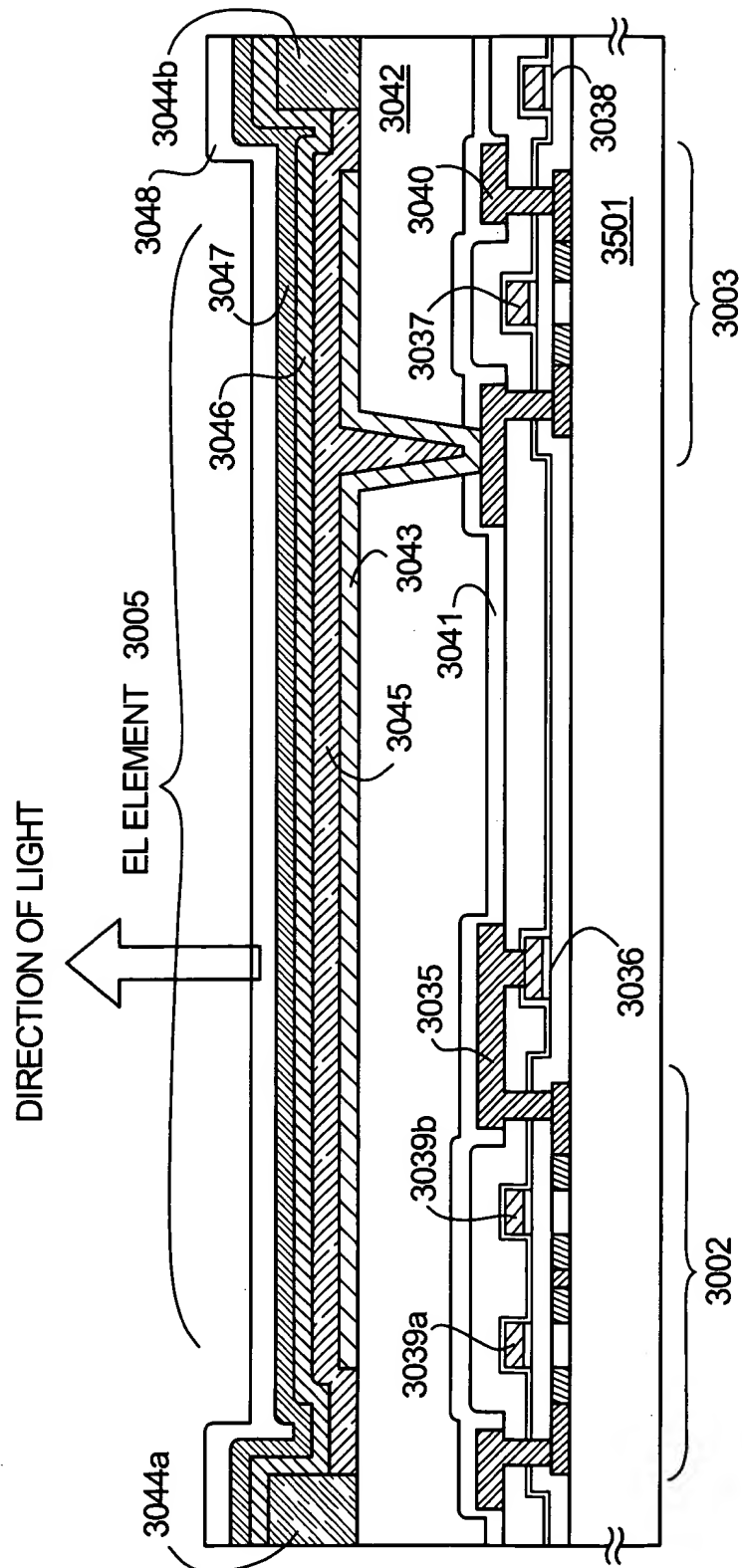


FIG. 34



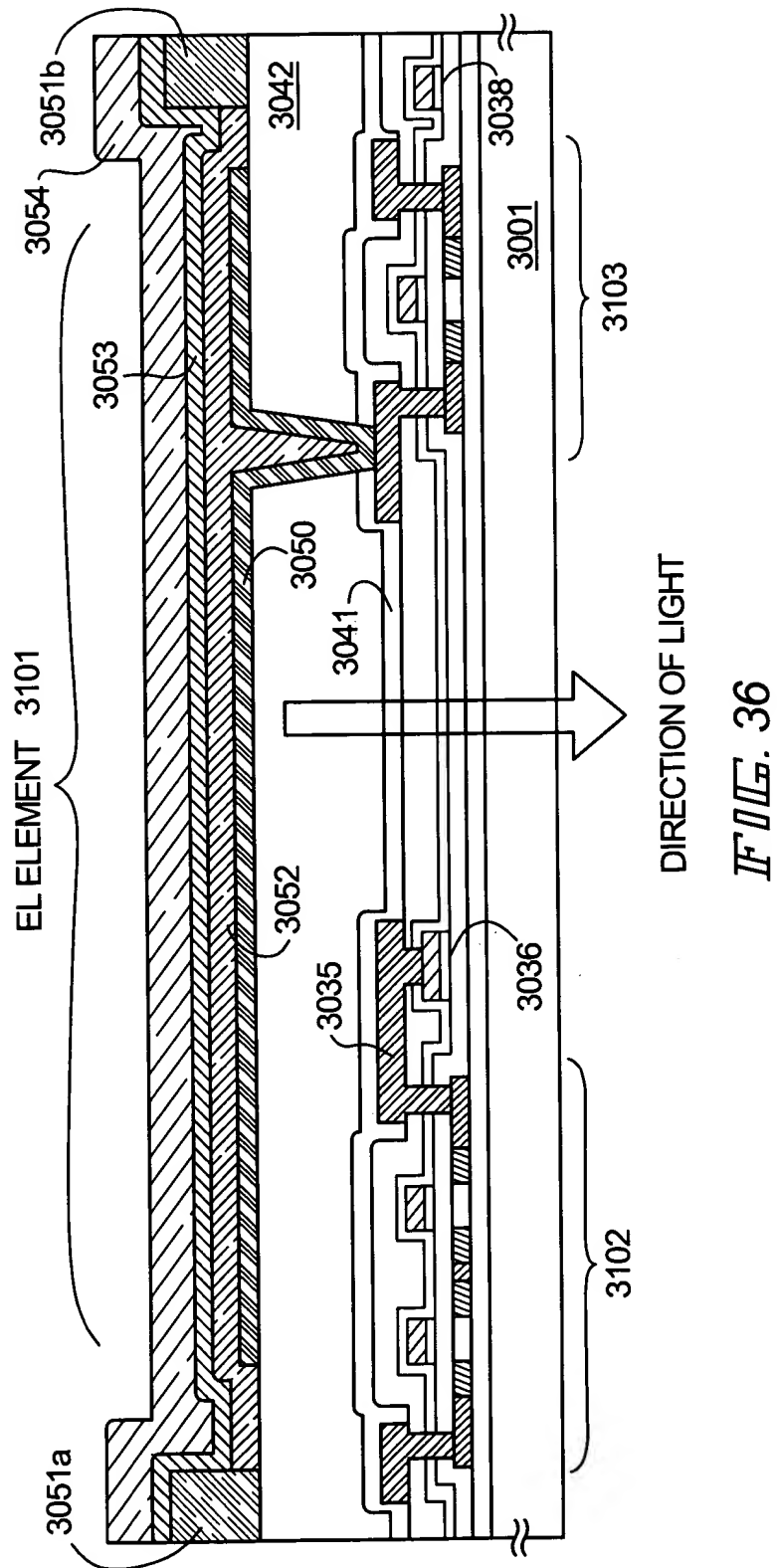


FIG. 36

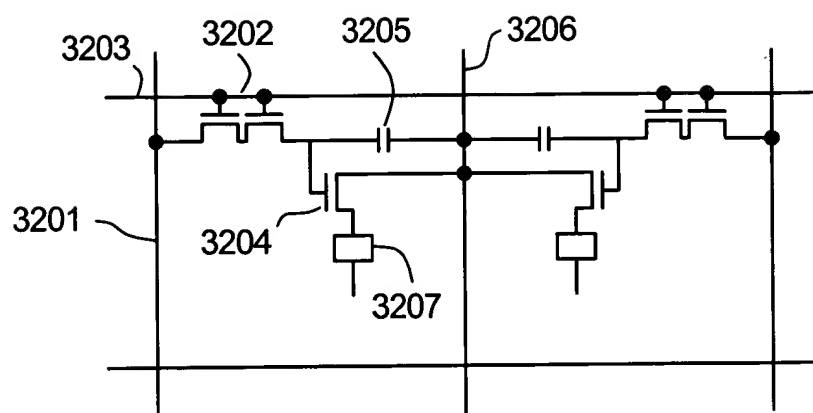


FIG. 37A

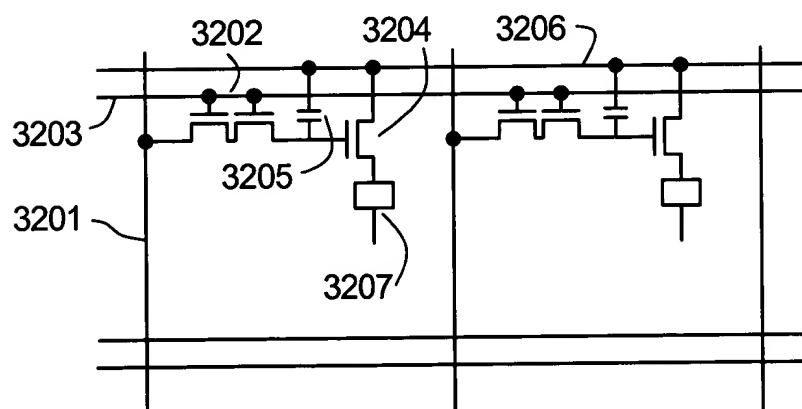


FIG. 37B

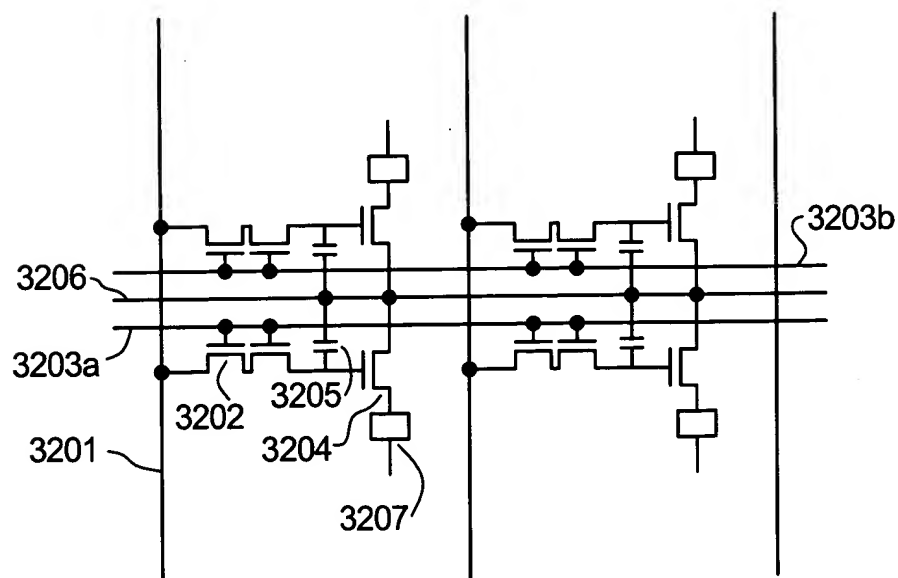


FIG. 37C